Docket No.: 28944/50048

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Travis Wade

Application No.: 10/599,127 Confirmation No.: 4575

Filed: September 20, 2006 Art Unit: Not yet assigned

For: Process for Fabricating Electronic Components Examiner: Not yet assigned

and Electronic Components Obtained By This

Process

## **INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom

This Information Disclosure Statement is filed within three months of the U.S. filing date (37 CFR 1.97(b)(1)).

Copies of the non-patent and non-U.S. references listed on the PTO/SB/08 are attached. For non-English language document DE 10036897, the English-language equivalent, U.S. Patent No. 6,740,910, is also attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR

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1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1 98 and the Examiner is respectfully requested to consider the listed references

Applicants believe that no fee is due at this time. However, the Director is hereby authorized to charge any fee(s) which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-3629, under Order No. 28944/50048.

Dated: November 1, 2006

Respectfully submitted,

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Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/B/PTO				Complete if Known			
	FORMATION DISCLOSURE	Application Number	10/599,127 - Conf. No. 4575				
IN	FORMATION	DIS	CLOSURE	Filing Date	September 20, 2006		
STATEMENT BY APPLICANT				First Named Inventor	Travis Wade		
]				Art Unit	Not yet assigned		
(Use as many sheets as necessary)				Examiner Name	Not yet assigned		
Sheet	1	of	1	Attorney Docket Number	29844/50048		

				U.S. PA	TEN	T DOCUMENTS				
Examiner Initials*	Cite	1		Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document			Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	No 1									
		6,740,910	05-	-25-2004	Roes	sner et al.				
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		Foreign Patent Document  Country Codes - Number 4-Kind Codes (if known)  WO 00/51186 A		Publication		Name of Patentee or	Pages Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
Examiner Initials*	Cite No 1			Date MM-DD-Y	Applicant of Cited Document	T <sup>6</sup>				
				08-31-2000					Clawson	
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book magazine journal serial symposium, catalog, etc.), date, page(s) volume-issue number(s) publisher city and/or country where published.	T²		
		International Search Report for International Application No PCT/FR2005/000682, dated July 5, 2005, 3 pages.			
		"Selective Growth of Carbon Nanotubes for Nanoscale Transistors", Won Bong Choi, et al., Advanced functional Materials, Wiley Intersciences, vol. 13, No. 1, January 2003, pp. 80-84.			
		"Growth of Carbon Nantubes on Anodic Aluminum Oxide Templates: Fabrication of a Tube-in-Tube and Linearly Joined Tube", Jin Seung Lee et al., Chemistry of Materials American Chem. Soc., Vol. 13, No. 7, July 2001, pp. 2387-2391.			
		"Ultrahigh-Density Nanotransistors by Using Selectively Grown Vertical Carbon Nanotubes", Wong Bong Choi et al , Applied Physics Letters, American Institute of Physics, November 2001, Vol. 79, No. 22, pp. 3696-3698.			
		"Electrochemical Fabrication of Cadmium Chalcogenide Microdiode Arrays", Chemistry of Materials, July 1993, Vol. 5, No. 7, pp. 902-904.			
		"Coulomb Blockade in a Single Tunnel Junction Directly Connected to a Multiwalled Carbon Nanotube", Applied Physics Letters, American Institute of Physics, October 2000, Vol. 77, No. 18, pp. 2891-2893.			

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609 Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

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